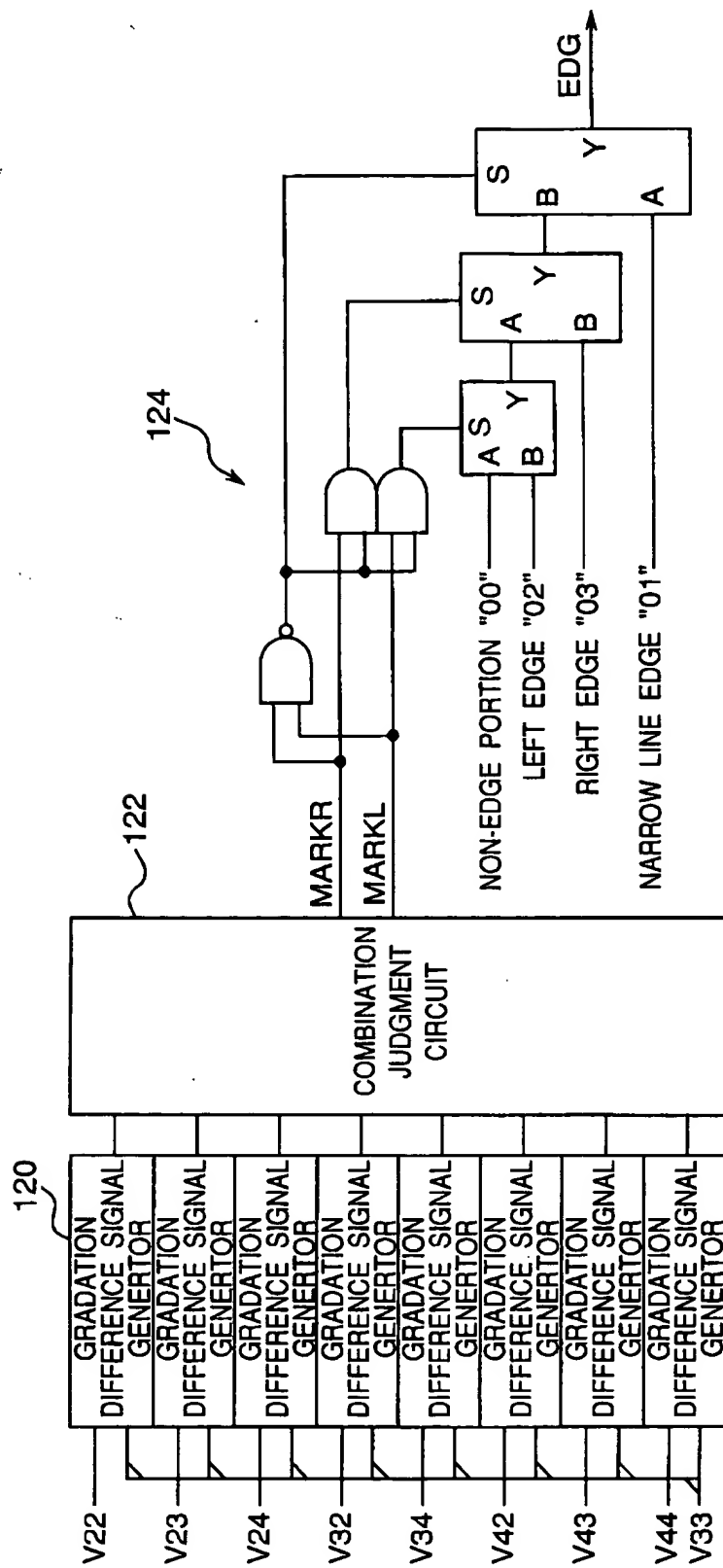


655260" 93680462

Fig.2

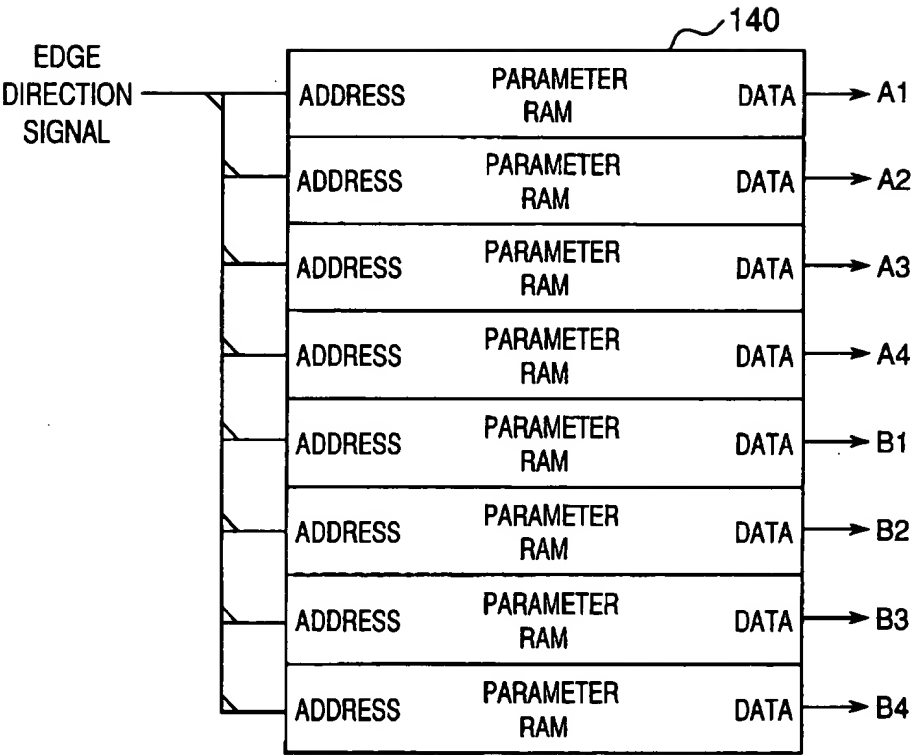


APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
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Fig.3

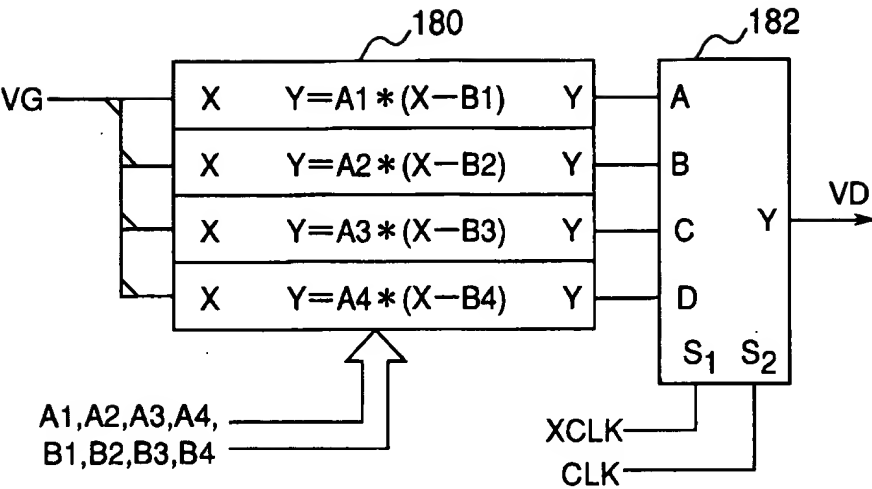
V22	V23	V24
V32	V33	V34
V42	V43	V44

Fig.4



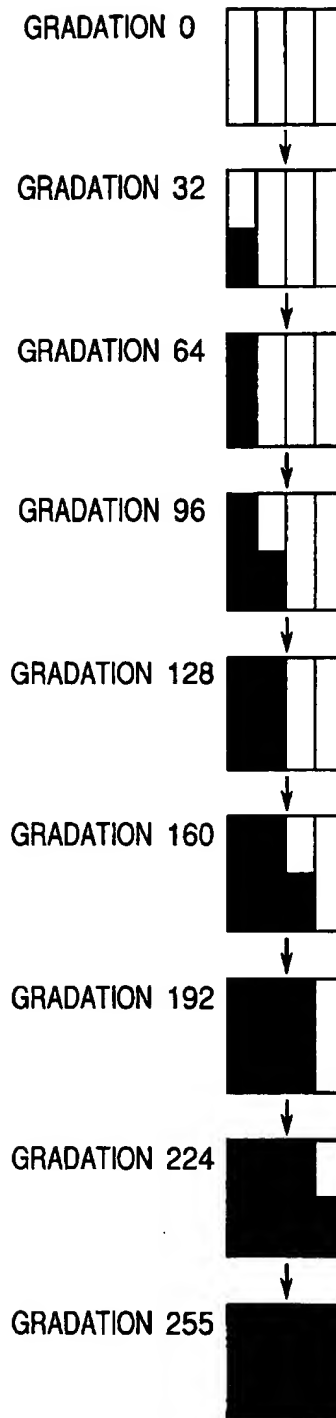
APPROVED	O.G. FIG.	
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Fig.5



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
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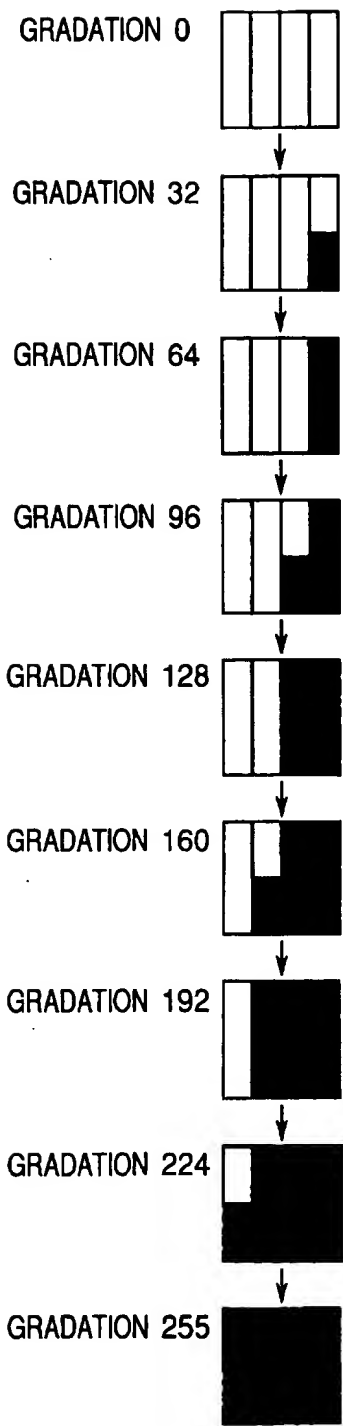
Fig.6



55252" 29292450

APPROVED	O.G. FIG.	
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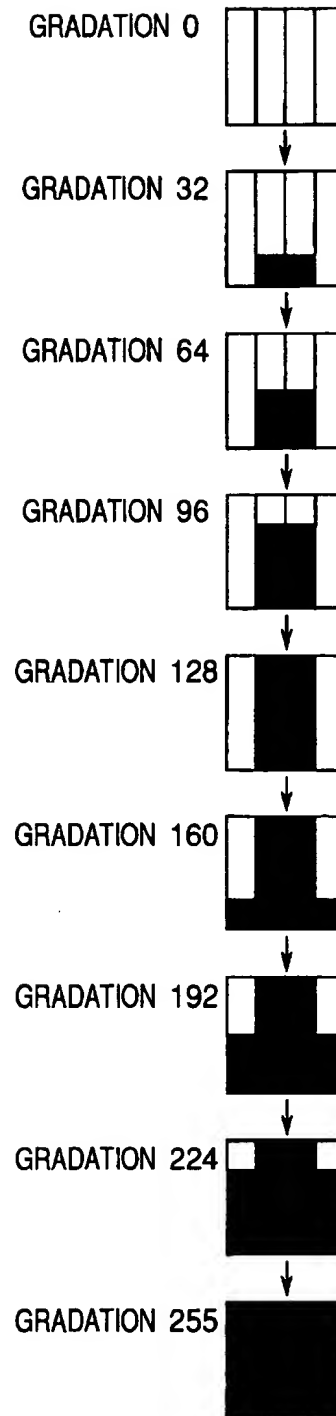
Fig.7



0 32 64 96 128 160 192 224 255

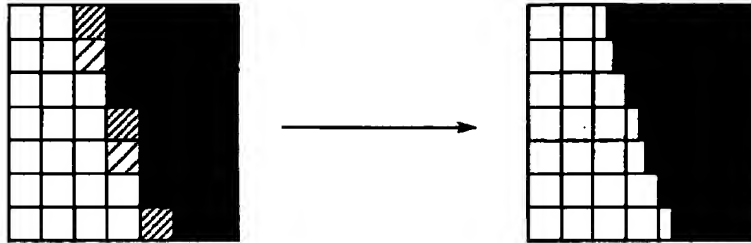
APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

Fig.9



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

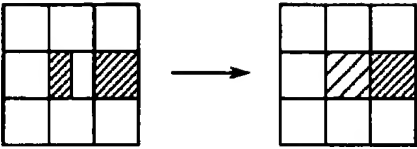
Fig. 10



65-2347-29000000

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

Fig.12

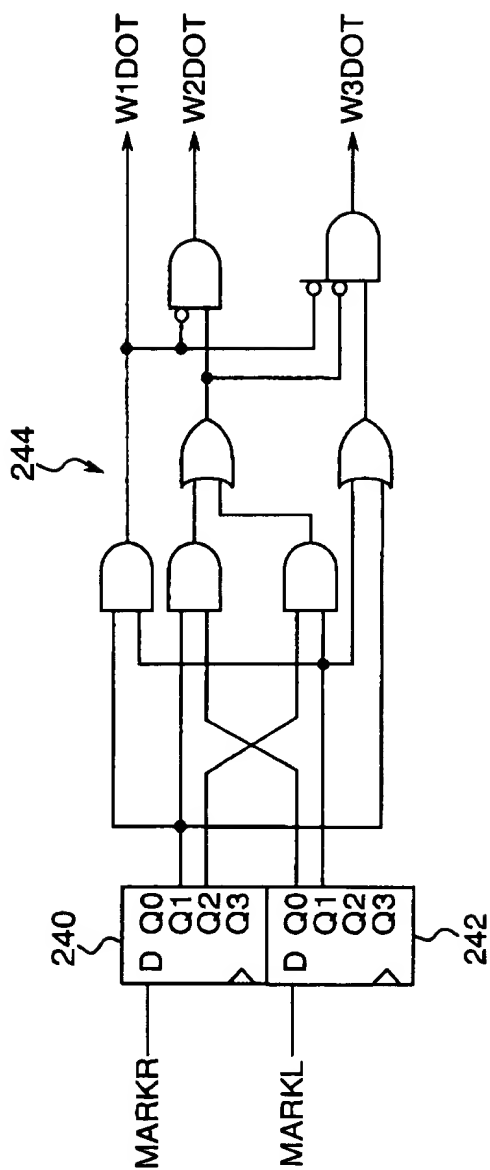


65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

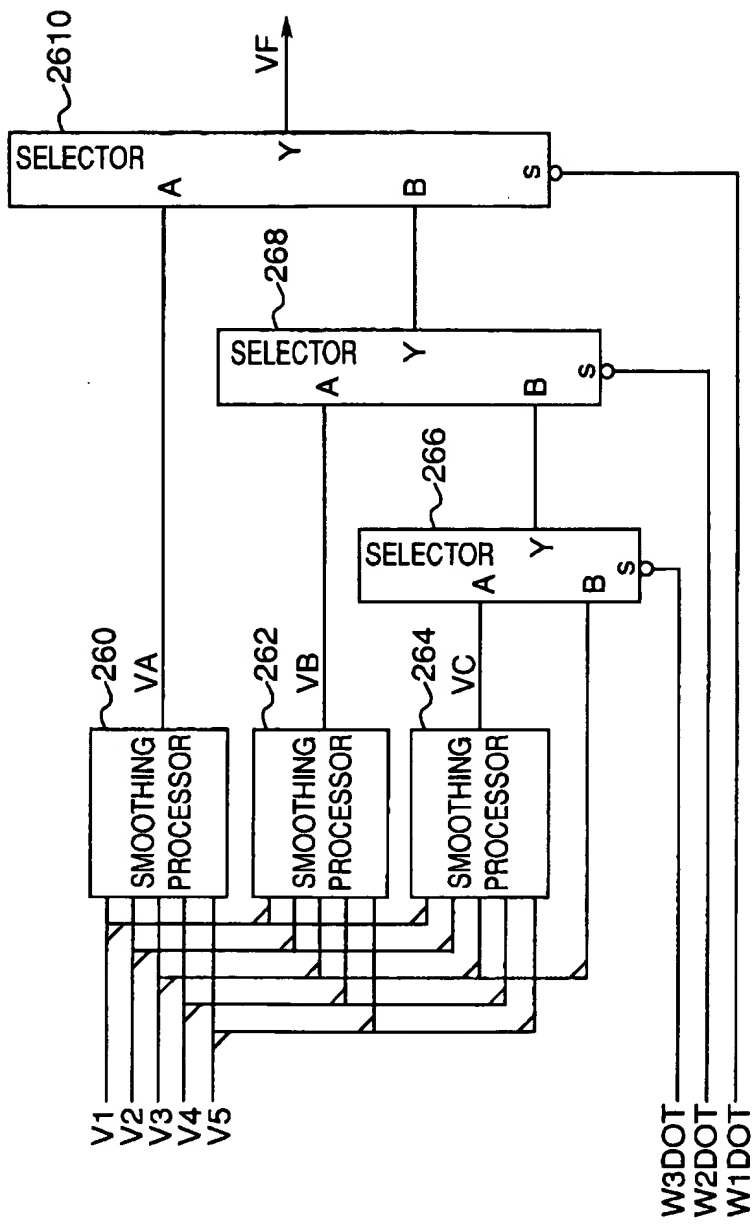
6662452 99090450

Fig. 14



666354" 22E 22450

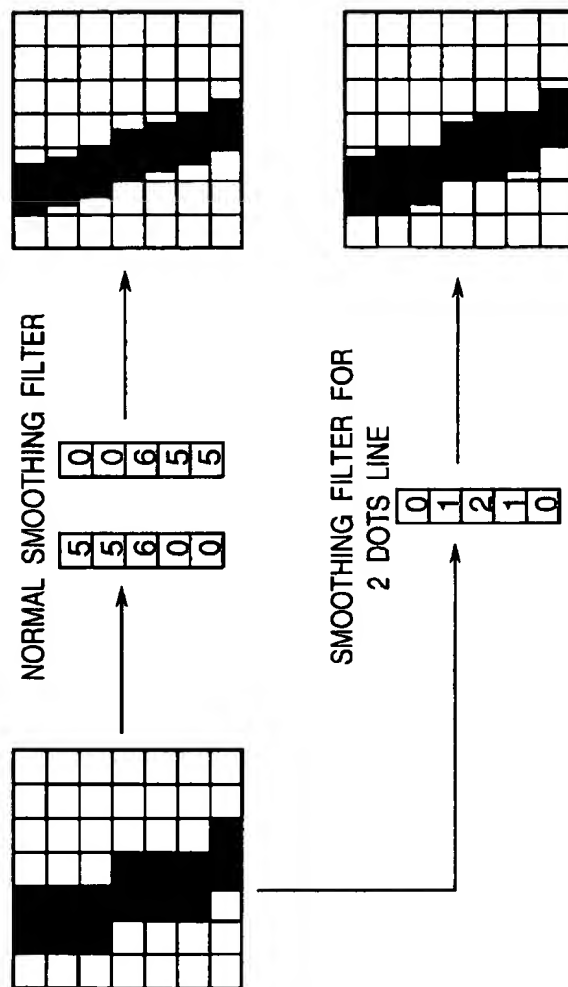
Fig. 15



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100

Fig. 16



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

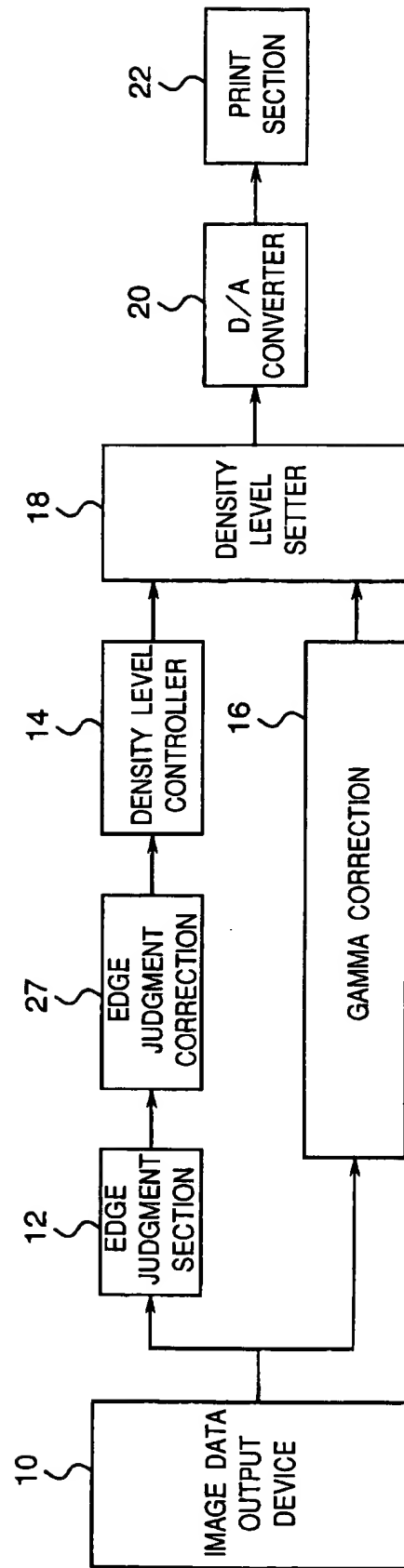
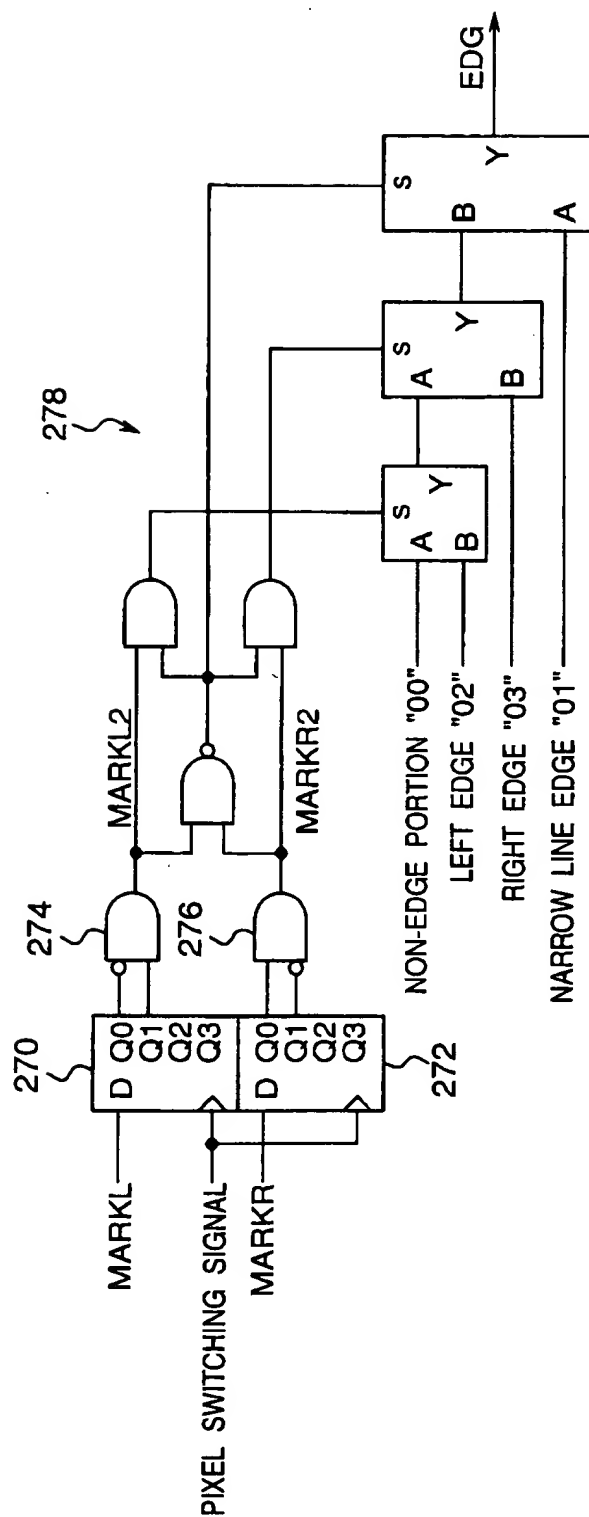


Fig. 17

55250-3920460

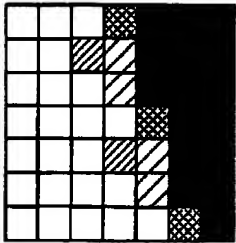
Figure 1 is a block diagram of a pixel edge detection circuit. The circuit includes two 4-bit shift registers, 270 and 272. Register 270 has inputs MARKL, PIXEL SWITCHING SIGNAL, and MARKR, and outputs Q0, Q1, Q2, and Q3. Register 272 has inputs MARKL, PIXEL SWITCHING SIGNAL, and MARKR, and outputs Q0, Q1, Q2, and Q3. The outputs of the shift registers are connected to a network of logic gates (AND, OR, NOT) to produce MARKL2 and MARKR2 signals. These signals are then connected to three 2-to-1 multiplexers (274, 276, and 278) which select between the outputs of the shift registers and the MARKL2 and MARKR2 signals to produce the final edge detection outputs: NON-EDGE PORTION '00', LEFT EDGE '02', RIGHT EDGE '03', and NARROW LINE EDGE '01'.



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Fig.19

COMPARISON EXAMPLE



ORIGINAL IMAGE

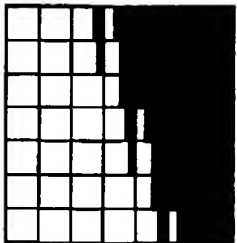
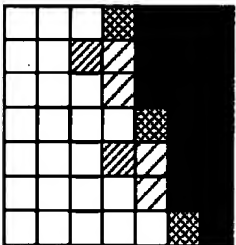


IMAGE AFTER
IMAGE PROCESSING

Fig.20



ORIGINAL IMAGE

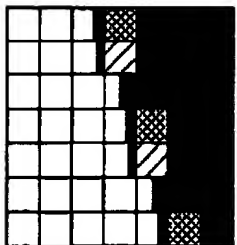
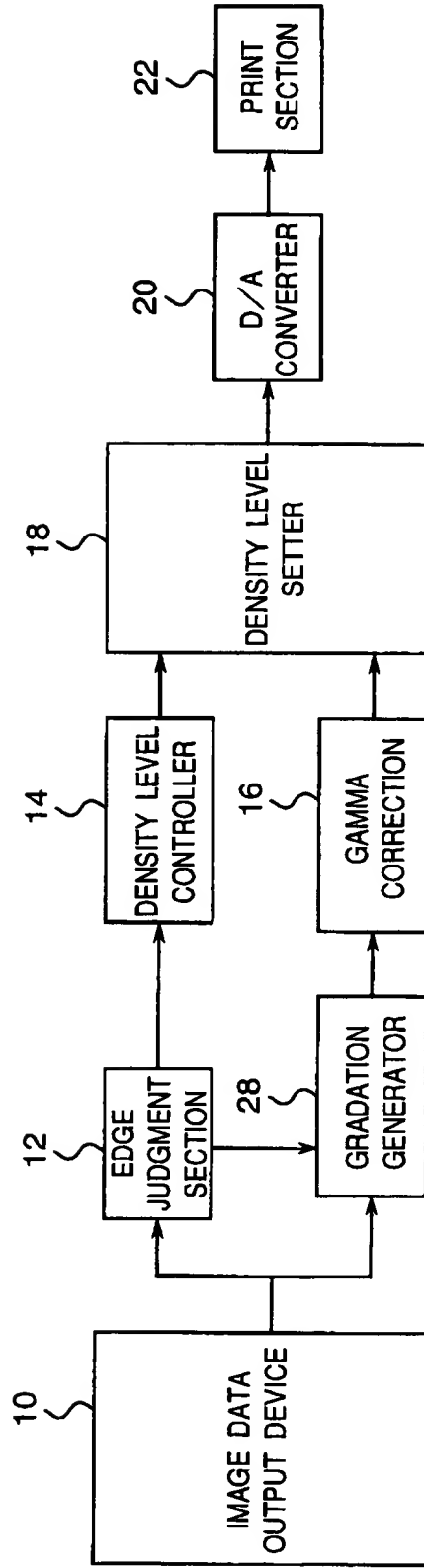


IMAGE AFTER
IMAGE PROCESSING

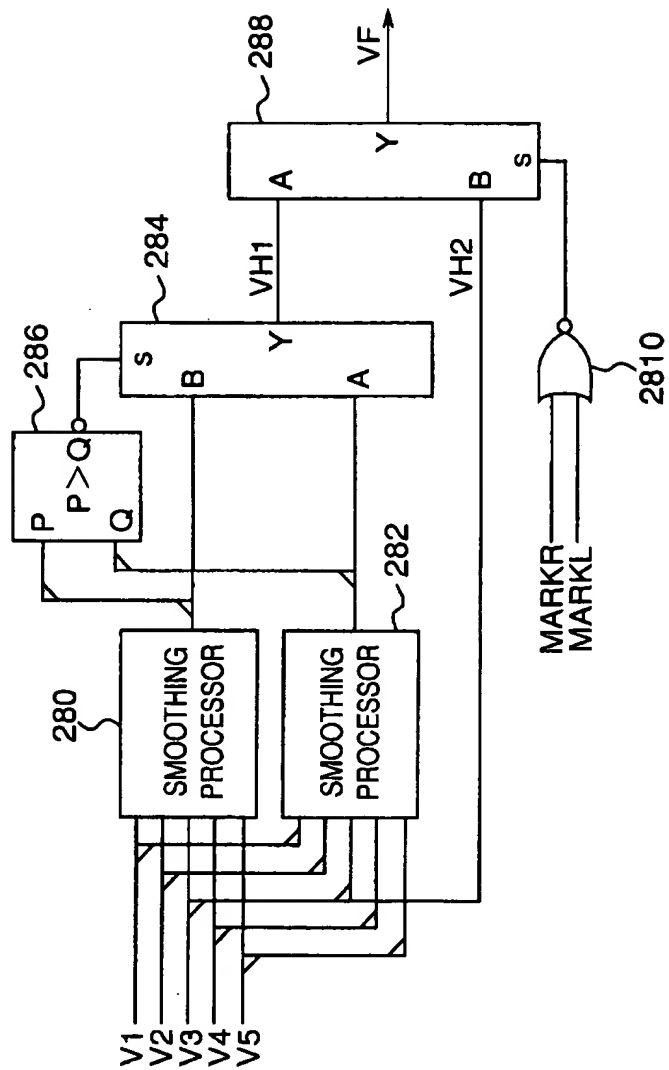
APPROVED	O.G. FIG.	
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Fig.21



APPROVED	O.G. FIG.	
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Fig.22



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Fig.23

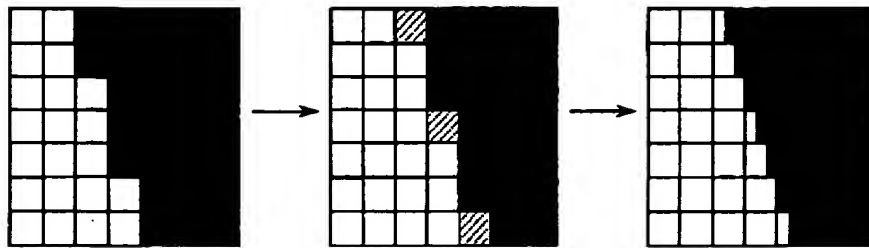


Fig. 24

